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In other words, an erase operation is always required to **write** data. The logical **block** number LBN is a **quotient** (=LSA/n) obtained by **dividing** the Inventor: Kikuchi6388919**Memory** controller for **flash memory** system and method for ... www.patentstorm.us/patents/6639843/description.html - Similar -

Storing device for writing data onto a plurality of installed ...

The storing device of claim 4, wherein, when a **block** containing storing regions to **write** data therein has data ... control method comprising steps of: calculating a **quotient** by **dividing** q by p, ... Inventor: Pettis, et al.5572466, **Flash memory** chips ... Compiler having automatic common **blocks** of **memory** splitting ...

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In order to rewrite data onto a **flash memory**, an entire **block** of the **flash** ... yielded by **dividing** total **write** size of each **flash memory** module through a ...

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However, in **flash memory**, an erase operation is performed on a **block** basis, ... the **quotient** obtained by **dividing** the value of a Logical **Block** Address (LBA) This **write** delay technique can increase the lifespan of **flash memory** by ... www.faqs.org/patents/app/20090106486 - Cached - Similar - William More results from www.faqs.org

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Dec 16, 2008 ... Power **Quotient** International Co., Ltd (Chung Ho, TW) ... "C|Net Hardware: Mini Megabytes: USB **flash** drives" Published Aug. The reserved **block** does not allow general end users to read, **write**, modify, delete data, ... Such hardware feature of **dividing** the **memory** into **blocks** is not found in ...

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This function is used for SMBus Read **Block** and SMBus **Write Block** protocol. It sends These routines can be used to erase and **write** to **flash** data **memory**. Returns the result of multiplying a by b and then **dividing** by c. The routine uses Returns the **quotient** a/b and the remainder. Input: r3:r2:r1:r0: ...

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(WO/1998/024029) FLASH MEMORY MASS STORAGE SYSTEM

One type of semiconductor nonvolatile **memory** is **flash memory** which is comprised of a 3 shows the status of **blocks** after the **write** operation of Figs. ... (A) Description of the System Architecture (1) **Dividing Memory** Cells into **Blocks** Fig. The **quotient** of this division is the logical **block** address and the ...

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The 32K-word embedded **flash** on SPMC75F2413A is divided into two **blocks**: the ... the 2K-word bank which can be programmed to be read-**write** mode can also be separated The checksum R is the remainder of **dividing** the binary data D by a ... r zeros at the end of D); Q(x) is the **quotient** of this division and R(x) is ...

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FOUND **BLOCK**. AS POINTER. **BLOCK** 526. I. **WRITE** POINTER. **BLOCK** TO space available in **flash memory**. FIGS. 2A, 2B and 2C are **block** diagrams ... 1. a computer system 100 is shown iQ during the S2D utility operation is set by **dividing** a number ... operating in each pointer **block**. The **quotient** and remainder resulting ... www.deli.com/downloads/giobal/corporate/patents/5754853 pdf - Similar - ...

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